

Substitute the following paragraph for the amended paragraph on page 21:

A header-symbol-pattern generator 158 outputs a replica of the header-symbol-sequence signal. The correlator 175 includes a quadrature-phase product device 148 and an in-phase product device 147, which are each coupled to the output of the matched filter 139 and to the header-symbol-pattern generator 158. An in-phase adder 149 and a quadrature-phase adder 151 are coupled to the output of the in-phase product device 147 and the quadrature-phase product device 148, respectively. A memory 152, which may be a register, is coupled to the outputs of the in-phase adder 149 and the quadrature-phase adder 151, and to the inputs of the in-phase adder 149 and the quadrature-phase adder 151. The output of the memory 152 is coupled to a peak detector 88 and to the in-phase product device 47 and the quadrature-phase product device 48, respectively. The peak detector 88 is coupled to the header-timing circuit 46.

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